UNITED STATES SECURITIES AND EXCHANGE COMMISSION Washington, D.C. 20549

FORM 8-K

CURRENT REPORT Pursuant to Section 13 or 15(d) of the Securities Exchange Act of 1934

Date of Report (Date of earliest event reported): October 20, 2025

	(Exact name of registrant as specified in its charte	r)		
Delaware	001-42570	85-2807351		
(State or other jurisdiction of incorporation)	(Commission File Number)	(IRS Employer Identification No.)		
27 Castilian Drive Goleta, California		93117		
(Address of principal executive off	fices)	(Zip Code)		
	805-351-2707 (Registrant's telephone number, including area cod	le)		
Check the appropriate box below if the Form 8-K filing is it	intended to simultaneously satisfy the filing obligation of	of the registrant under any of the following provisions:		
☐ Written communications pursuant to Rule 425 under the	ne Securities Act (17 CFR 230.425)			
☐ Soliciting material pursuant to Rule 14a-12 under the I	Exchange Act (17 CFR 240.14a-12)			
☐ Pre-commencement communications pursuant to Rule	14d-2(b) under the Exchange Act (17 CFR 240.14d-2(b)	b))		
☐ Pre-commencement communications pursuant to Rule	13e-4(c) under the Exchange Act (17 CFR 240.13e-4(c)	2))		
Securities registered pursuant to Section 12(b) of the Ac	et:			
Title of each class	Trading Symbol(s)	Name of each exchange on which registered		
Common Stock	ALMU	The Nasdaq Stock Market LLC		
Indicate by check mark whether the registrant is an emergi the Securities Exchange Act of 1934 (§240.12b-2 of this ch		urities Act of 1933 (§230.405 of this chapter) or Rule 12b-2 of		
		Emerging growth company		
If an emerging growth company, indicate by check mark is accounting standards provided pursuant to Section 13(a) of		sition period for complying with any new or revised financial		

Item 8.01 Other Events.

We are filing this report to disclose our new investor PowerPoint presentation. The presentation is furnished as an exhibit to this Current Report on Form 8-K.

Neither this report nor the exhibit attached hereto constitute an offer to sell, or the solicitation of an offer to buy our securities, nor shall there be any sale of our securities in any state or jurisdiction in which such offer, solicitation or sale would be unlawful prior to the registration or qualification under the securities laws of any such state or jurisdiction.

Item 9.01: Financial Statements and Exhibits

(d) Exhibits

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Number	Exhibit
99.1	<u>Presentation</u>
104	Cover Page Interactive Data File (embedded within the Inline XBRL document)
101	Cover rage intolactive Sam rise (embedded within the limite risk document)

SIGNATURE

Pursuant to the requirements of the Securities Exchange Act of 1934, the registrant has duly caused this report to be signed on its behalf by the undersigned hereunto duly authorized.

AELUMA, INC.

By: /s/ Jonathan Klamkin Date: October 20, 2025

Jonathan Klamkin President, Chief Executive Officer, and Director



Forward Looking Statements



This presentation contains summary information about Aeluma, Inc. ("Aeluma") as of the date hereof. The information in this presentation is of general background and contains an overview and summary of certain data selected by the management of Aeluma. It does not purport to be complete.

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Background Story: Gallium Nitride (GaN) and the Blue LED



A materials breakthrough that enabled an industry and more

"These won't ever replace the kitchen light."

"The research is a waste of money."



The blue LED revolutionized lighting and displays, and gallium nitride broadly impacted many photonics and electronics applications.

LEDs use 75% less energy and last 25 times longer

Energy savings by 2035 of 569 TWh (92 1,000 MW power plants)





https://www.youtube.com/watch?v=AF8d72mA41M&t=1399s

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LED: light emitting diode; www.energy.gov/energysaver/led-lighting 4

Aeluma's Indium Gallium Arsenide (InGaAs) Breakthrough



Best-in-class materials with scalable manufacturing

"InGaAs on silicon won't work, people have tried."
"You can't grow quantum dots by MOCVD."



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InGaAs is the best material for shortwave infrared (SWIR) applications, impacting defense & aerospace, mobile and consumer electronics, Al infrastructure, industrial and robotics, quantum, and automotive.

Eye safe, operation in direct sunlight, night vision, higher sensitivity, higher speed, lower power consumption, and lower cost



MOCVD: metalorganic chemical vapor deposition; Outcomes cannot be guaranteed.

Investment Highlights



Derisked business model poised for growth as U.S. recaptures semiconductor preeminence



Rapidly Expanding SAM \$4.9B in 2030

Broad applications across Defense and Aerospace, Mobile and Consumer Electronics, Al Infrastructure, Quantum, Industrial and Robotics, and Automotive



Transformative Technology

- Best-in-class materials with large-volume manufacturing
- · Highest performance at scale
- 30+ issued and pending patents



Significant Business Traction

- Disruptive tech validated by U.S. agencies and customers
- Sampling, small volume orders, and NRE contracts
- 20 customer engagements



Capital-Light Manufacturing

- Primarily outsourced manufacturing for rapid scaling
- Some proprietary steps carried out in house
- ISO9001:2015 certified



Clean Balance Sheet

- No debt or overhang
- \$39.2M cash (pro-forma at 6/30)



Attractive Financial Profile and Outlook

- Recurring R&D revenue
- Additional capital to fund business and accelerate commercialization

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SAM estimate based on internal projections; NRE: Non-recurring engineering; Outcomes cannot be guaranteed.

Recent Developments

FY2025 Results Announced on 9/9/25



Operational and Financial Highlights

- ✓ Nasdaq IPO (Uplist) and \$13.8M capital raise In March
- √ \$25.4M capital raise in September
- ✓ FY2025 revenue \$4.7M (up from \$0.9M in FY24)
- √ 6 R&D contracts secured in 2025, including 2 in Q4
- √ 20 active customer engagements
- ✓ Unveiled scalable manufacturing process applicable to quantum computing and communication systems
- ✓ Chris Stewart with strong semiconductor and publiccompany experience appointed CFO
- ✓ Strong balance sheet with \$39.2M in Cash and Cash Equivalents (pro-forma at 6/30) and no debt

Strategic Priorities for FY2026

□ New Contract Wins → 3-7 new R&D contracts

- □ Team Expansion → Accelerate hiring of business development and go-to-market team, technical leadership, and operations
- □ Enhanced Manufacturing Readiness → Higher levels of wafer fab activities, expanded test and validation capabilities, technology qualification for target industries
- □ Go-to-Market Traction → Continued progress with engaged targeted commercial markets and increase customer engagements in pipeline

Actively engaged with 20 prospective customers evaluating our technology for potential integration

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Outcomes cannot be guaranteed.

Aeluma Address Multiple High-Growth Markets

Aeluma™

High-performance semiconductors that scale

Mobile, Tablet, PC, and AR/VR





- Depth perception
- Facial ID
- 3D scanner
- · Health monitoring
- AR/VR

Defense and Aerospace





- 3D imaging and LiDAR
- Security
- Autonomous systems
- Atmospheric sensing
- Topography

Al, DCI and Quantum





- Al infrastructure
- Data centers and HPC
- Telecommunications
- Quantum
- 5G/6G wireless

Automotive





- Consumer vehicles
- Robotaxis
- Trucking
- Advanced driver assistance systems

Industrial and Robotics





- Robotics
- Delivery robots
- Factory automation
- Logistics
- Security

Aeluma is positioned as a semiconductor technology provider to service a broad range of market verticals.

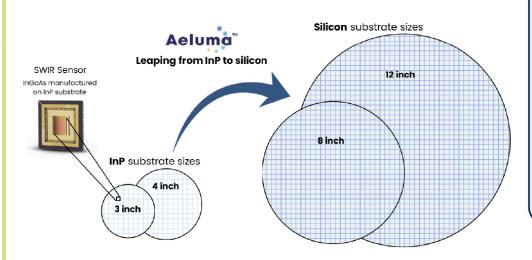
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DCI: data center interconnects; HPC: high-performance computing.

Aeluma's Breakthrough in Semiconductor Manufacturing



Best-in-class materials on large-diameter substrates





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Source of SWIR sensor image: https://www.flir.com/support/products/swir-ingaas-fpa/ 9

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Aeluma's Technology Breakthrough



Scalable, cost-effective manufacturing enabled by cutting-edge intellectual property

Conventional manufacturing of InGaAs semiconductor devices



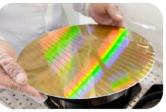


Moving from 3-inch to 12-inch wafers

16X wafer area

Not scalable, manual, and low throughput

Aeluma high-performance InGaAs with silicon manufacturing







- \checkmark Highly automated and ability to produce many chips per wafer
- ✓ Monolithic CMOS process integration
- ✓ Wafer-scale integration and packaging
- √ 10X lower manufacturing cost for mass market applications

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 ${\it CMOS: complementary metal-oxide semiconductor; Outcomes \ cannot \ be \ guaranteed.}$

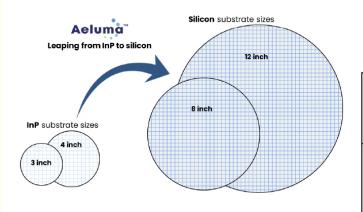
Manufacturing for a Mass Market



Aeluma's large-diameter manufacturing economies of scale

Aeluma's Large-Diameter Wafer Platform

Manufacturing Capacity Bottleneck: Producing 20 million sensor chips for consumer market



	Substrate size	Wafers required for 20m sensor chips	Typical fab capacity
Incumbent Technology	3-inch	425k wafers	1k-10k wafers per month
	4-inch	213k wafers	1k-10k wafers per month
Aeluma™	8-inch	43k wafers	10k-100k per month
Large-diameter platform	12-inch	18k wafers	10k-100k per month

Incumbent technologies cannot meet volume requirements for consumer markets Aeluma's manufacturing enables scaling and cost reduction required for current mass market applications

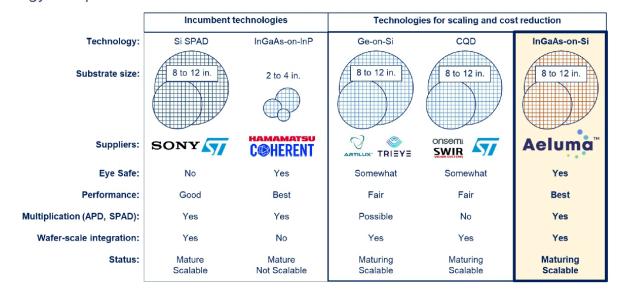
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 $Values \ are \ provided \ for \ qualitative \ illustration \ purposes \ only; Outcomes \ cannot \ be \ guaranteed. \qquad 11$

Aeluma Outperforms the Competition



Technology Comparison



Aeluma's is the only known technology that combines proven, high-performance InGaAs with scalable, cost-effective silicon manufacturing, thereby overcoming the cost-performance tradeoff.

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Metrics provided for qualitative illustration purposes only; SI: silicon; Typical InP substrate sizes are 3- or 4-inch; Typical SI substrate sizes are 8- or 12-inch.;
APD: Avalanche photodiode; SPAD: Single-photon avalanche diode; Outcomes cannot be guaranteed.

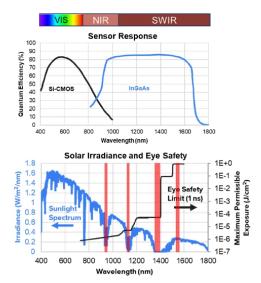
Why Aeluma and Why Now?





Market Vertical Example 1: Shortwave Infrared (SWIR) Sensors for Mobile and Consumer Electronics

What is SWIR?



SWIR sensors needed for eye safety and other benefits



Radical approach required to scale and reduce cost

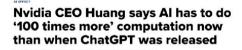
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VIS: visible; NIR: near infrared; SWR: shortwave infrared; Source of plots: Rep. Prog. Phys. 85, 12 (2022).

Why Aeluma and Why Now?



Market Vertical Example 2: Al Infrastructure and Data Center Interconnects

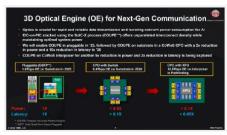






Overcoming the data transfer bottleneck:

The need for high-bandwidth, low-latency, and energyefficient optical interconnects





Aeluma™

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NVIDIA 2025 GTC Conference; www.trgdatacenters.com. 14

Why Aeluma and Why Now?



Market Vertical Example 3: Defense and Aerospace













A New Era in Defense Tech

\$130B venture capital invested in defense over four years

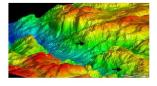






Mission-critical systems require high performance and operation in harsh environments









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www.warontherocks.com 15

Progression to Commercialization



Stages of the Customer Opportunity Pipeline

Phase 1 Phase 2 Phase 3 Phase 4 Phase 5



Discovery

- Marketing and trade shows
- Targeting customers
- Initial meetings



Technology Evaluation

- Technical and business discussions
- Execution of NDA
- Share proprietary technical information
- Customers share desired specs
- Customer requests samples for evaluation



NRE

- Custom development toward customer specs
- Generation of prototypes
- Small volume samples
- MOU execution considered



Qualification

- Commitment from customer for initial qualification
- Design win
- Qualification of production process
- Test and validation



Production

- Design review
- Finalize production process
- Initiate production
- Product delivery

20 active engagements in Phases 2 and 3

→Advance engagements and add more to pipeline

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Aeluma's Cost-Effective and Scalable Manufacturing Aeluma



Transitioning to Commercialization

12-inch wafer capability







Rapid prototyping and smallvolume manufacturing

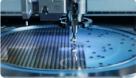




Partners for medium- and largevolume manufacturing







Combining rapid prototyping capabilities and volume manufacturing partners, Aeluma is positioned to effectively transition from R&D to commercialization with a capital-light approach.

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Aeluma's Headquarters



Ideal Location for Development and Commercialization



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Aeluma's Headquarters



Ideal Location for Development and Commercialization



Aeluma's Headquarters: Recent Expansion



Ideal Location for Development and Commercialization



Technology Portfolio

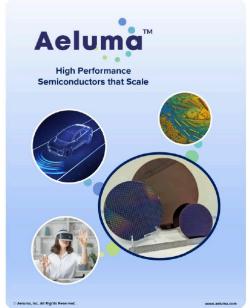


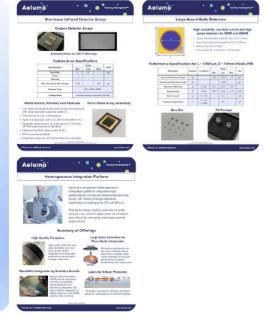
Near-Term Focus

- Detector Arrays
- Large-area Detectors
- High-speed Detectors
- Quantum Dot Lasers
- Templates

Longer Term

- QD Lasers in Silicon Photonics
- Quantum Photonics
- Nano-scale Semiconductors





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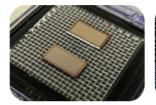
SWIR Photodiode Arrays

Active and Passive Imaging Sensors

Shortwave Infrared (SWIR) Array Features

- Low dark current photodetector arrays manufactured with large-diameter substrate platform
- Pixel and array size customizable
- Typical array sizes: 128X32, 256X128, 640X512
- Delivered as PDA chips or with ROICs
- FPA assembly available
- Small test arrays (ex. 8 X 8) available for evaluation/qualification

Photodetector Array Chips





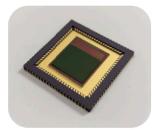
Examples shown are 256X128 format

Evaluation Board



Focal Plane Array Assembly

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Applicable markets include:

- Mobile and Consumer
- Industrial and Robotics
- Defense & Aerospace
- Automotive

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PDA: photodetector array; ROIC: read-out integrated circuit; FPA: focal plane array 22

Large-Area Photodiodes

Sensors

Photodiode Features

- Typical Photosensitive Diameter (D): 0.25 to 5.0mm (0.25, 0.5mm most common)
- Typical Operating Wavelength (λ): 0.95 to 1.55μm
- Device Type:

PIN, APD or SPAD (PIN and linear-mode APD most common)

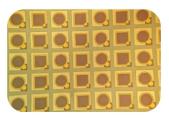
Sample Format:

Bare die, mounted on carrier or mounted in TO package

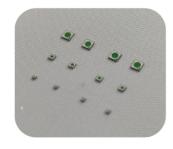
Photodiode Schematic



Fabricated Photodiodes



Bare Die



TO Package



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Applicable markets include:

- Mobile and Consumer
 Industrial and Robotics
- Defense & Aerospace Automotive

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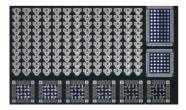
High-Speed Photodiodes

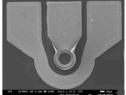
High-speed Optical Communications

High-speed Photodiode Features

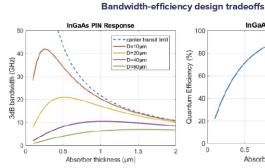
- Typical Operating Speed: 10-50 Gbps
- Typical Operating Wavelength (λ): 0.85 to 1.55μm
- Device Type:
 PIN, UTC-PD, APD
- Sample Format:
 Bare die, mounted on carrier, array format, wafer-scale integrated
- Target Architecture: Short-reach multi-mode fiber links and SWDM

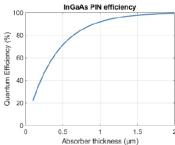
Fabricated high-speed InGaAs PIN photodetectors





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Applicable markets include:

- Defense & Aerospace
- Data Center Interconnects (DCI) and AI Infrastructure
- Telecor

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Quantum Dot Lasers

High-speed Optical Communications

Quantum Dot Lasers and Amplifiers

- Typical Operating Wavelength (λ): 1.25 to 1.35μm
- Device Format:

Fabry-Perot Laser, SOA, RSOA, DFB Laser

Sample Format:

Bare die, mounted on carrier, array format, integrated in SiPh

Target Architecture:

Single-mode fiber applications, multi-wavelength comb sources (WDM), Co-packaged optics (CPO)

NVIDIA's co-packaged optics platform with external lasers



*Source: NVIDIA GTC 2025

Aeluma™

External QD lasers for high efficiency, high temperature performance, and wavelength division multiplexing



*Source: CS Magazine, 30, 7, 2024

Integrated QD gain in silicon photonics



*Y. Bian et al., IEEE JSTQE, 29, 2023

Applicable markets include:

- Data Center Interconnects (DCI) and AI Infrastructure
- Telecom
- Mobile and Consumer
- Defense & Aerospace

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Leadership Team

Vision, Entrepreneurship, and Expertise

Senior Management



Jonathan Klamkin, PhD Founder, CEO & Director













Investors/Advisors



Shuji Nakamura, PhD Seed Investor



UCSB





Richard Ogawa, JD Seed Investor & Advisor



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Steven DenBaars, PhD Seed Investor & Director



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